

Figure 1, prior art

The diagram illustrates a prior art video encoding process (100). It begins with an 8x8 block of pixels (105), which is processed by a DCT block (110) to produce DCT coefficients (115). These coefficients are then quantized (120) to produce quantized DCT coefficients (125). A zig-zag scan (150) is applied to the quantized coefficients, resulting in a sequence of values (155). This sequence is then processed by a Run Length Encoding (RLE) block (160) to produce a Run Level Last (RLL) matrix (165). Finally, a Variable Length Encoding (VLE) block (170) processes the RLL matrix to produce the final VLE output (170). The diagram also shows a detailed view of the zig-zag scan (150) and the RLE block (160) processing the sequence (155) to produce the RLL matrix (165).

Figure 3, prior art

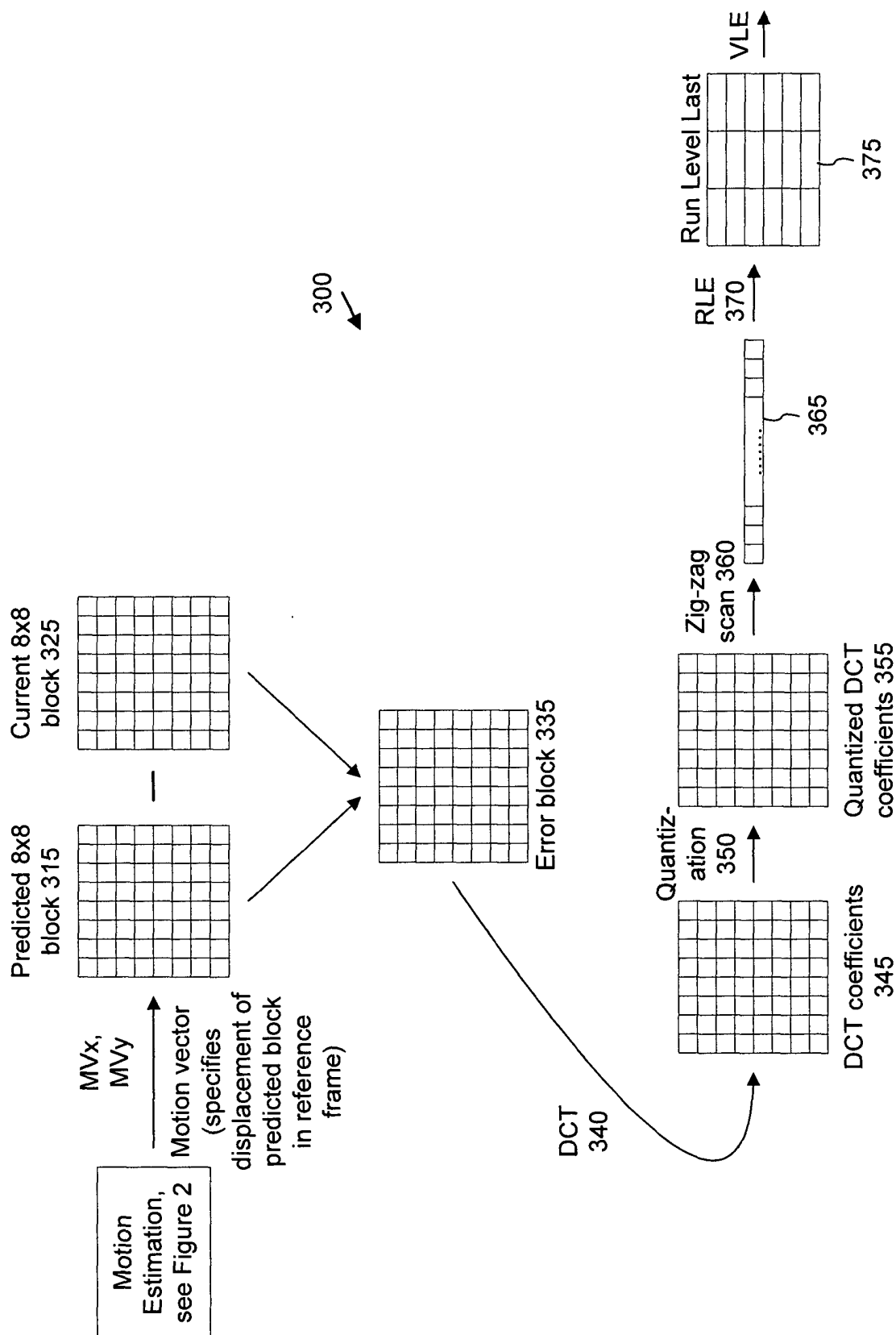
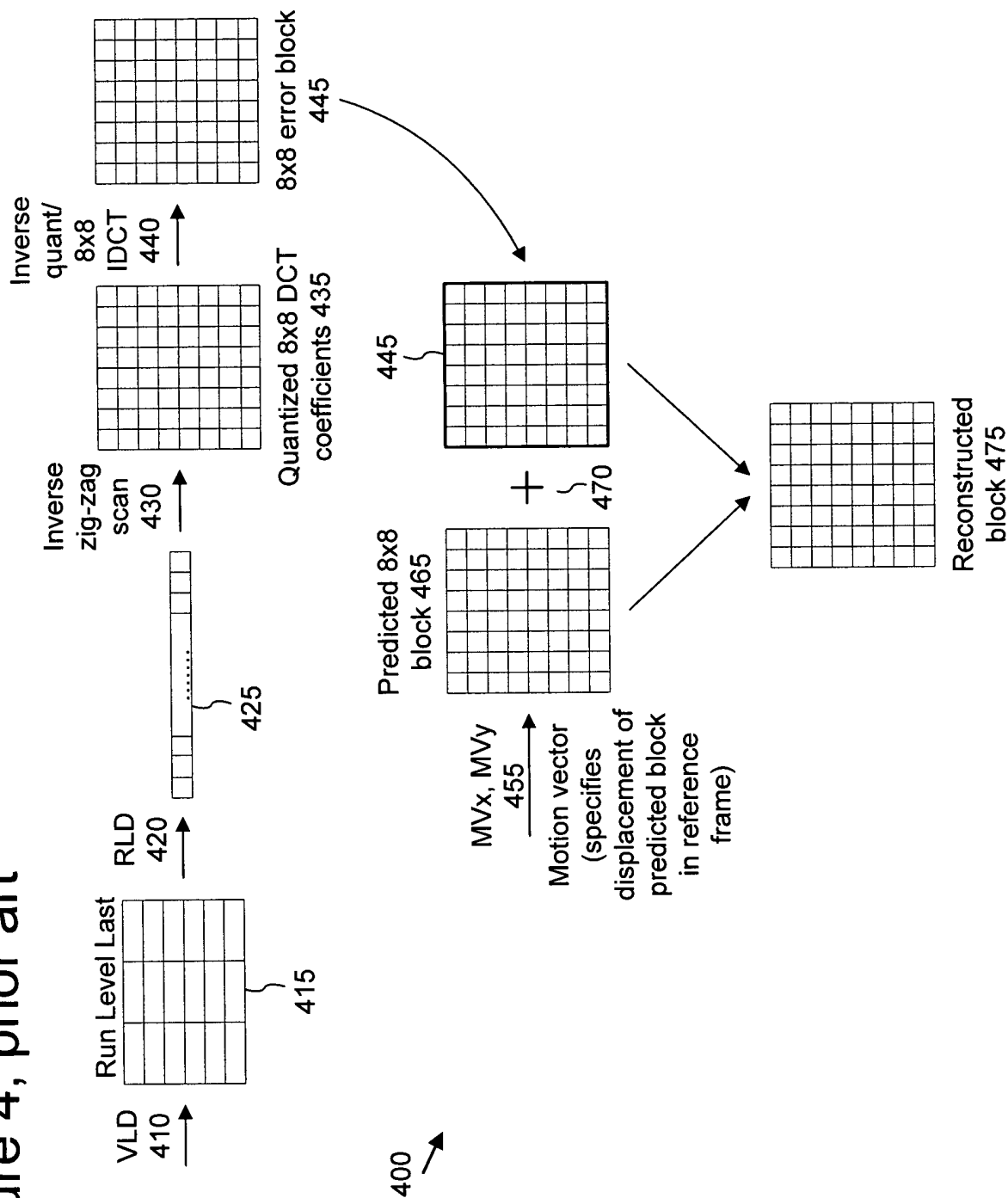


Figure 4, prior art



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Figure 5, prior art

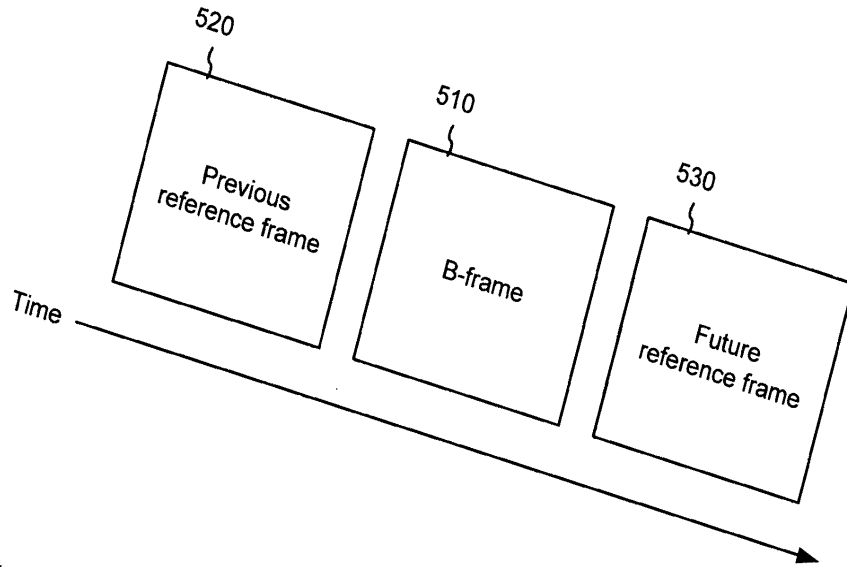


Figure 6, prior art

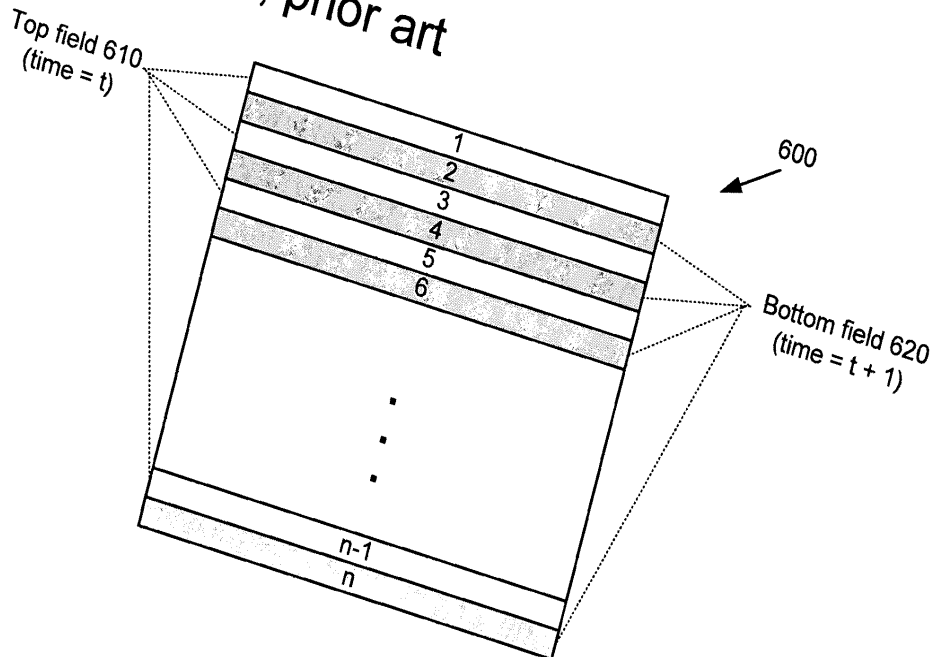


Figure 7

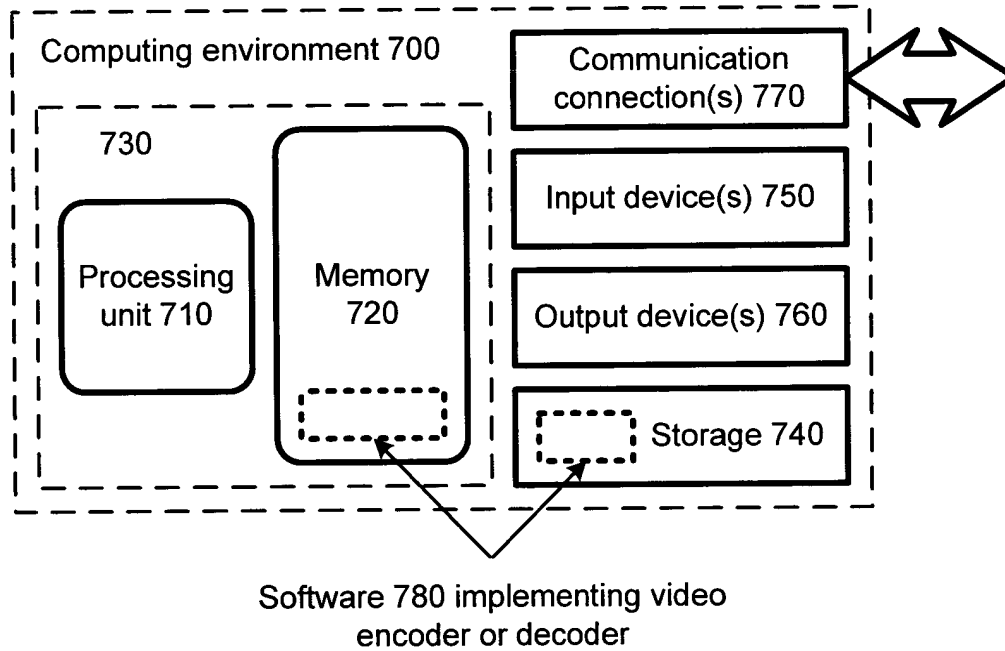


Figure 8

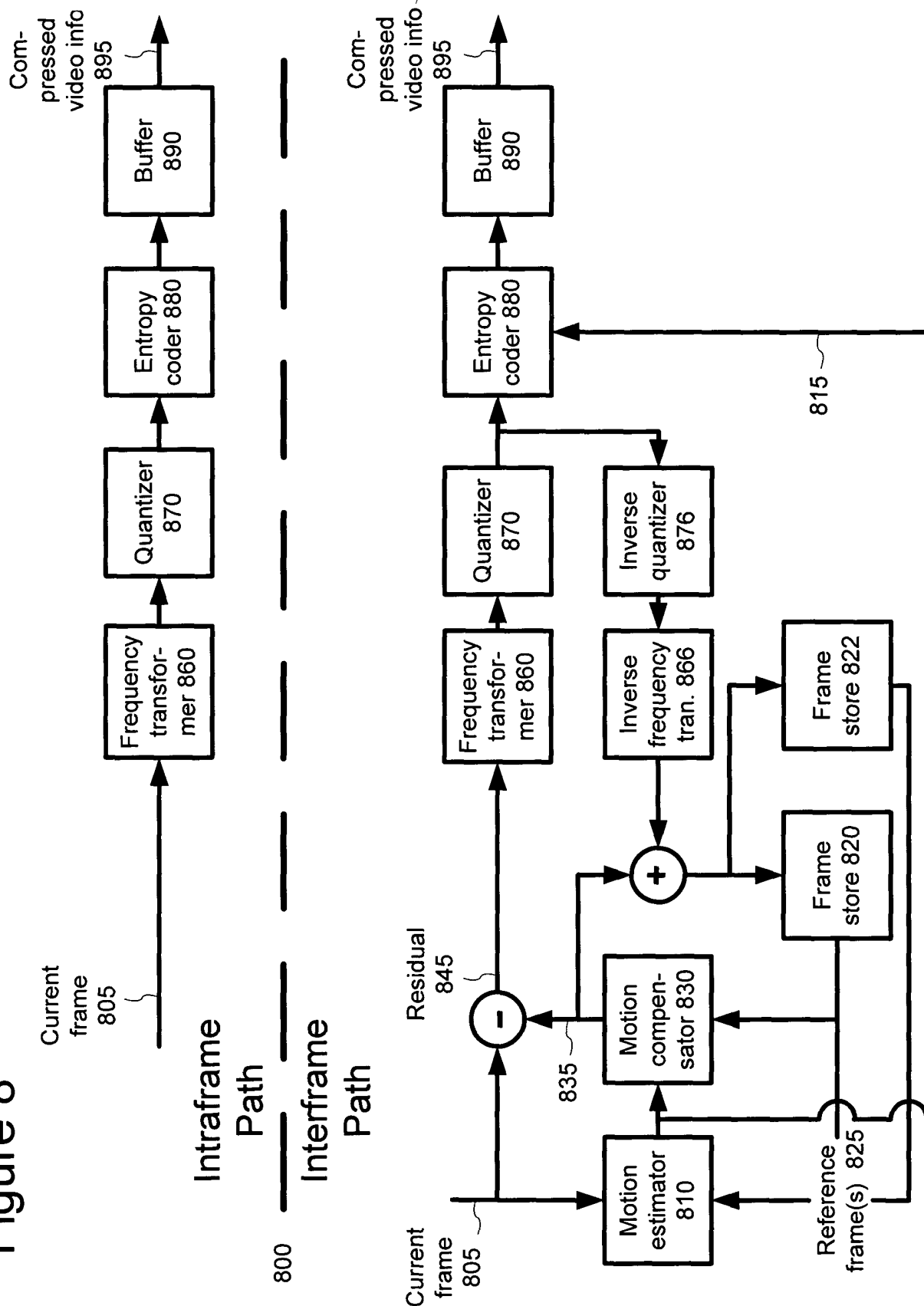


Figure 9

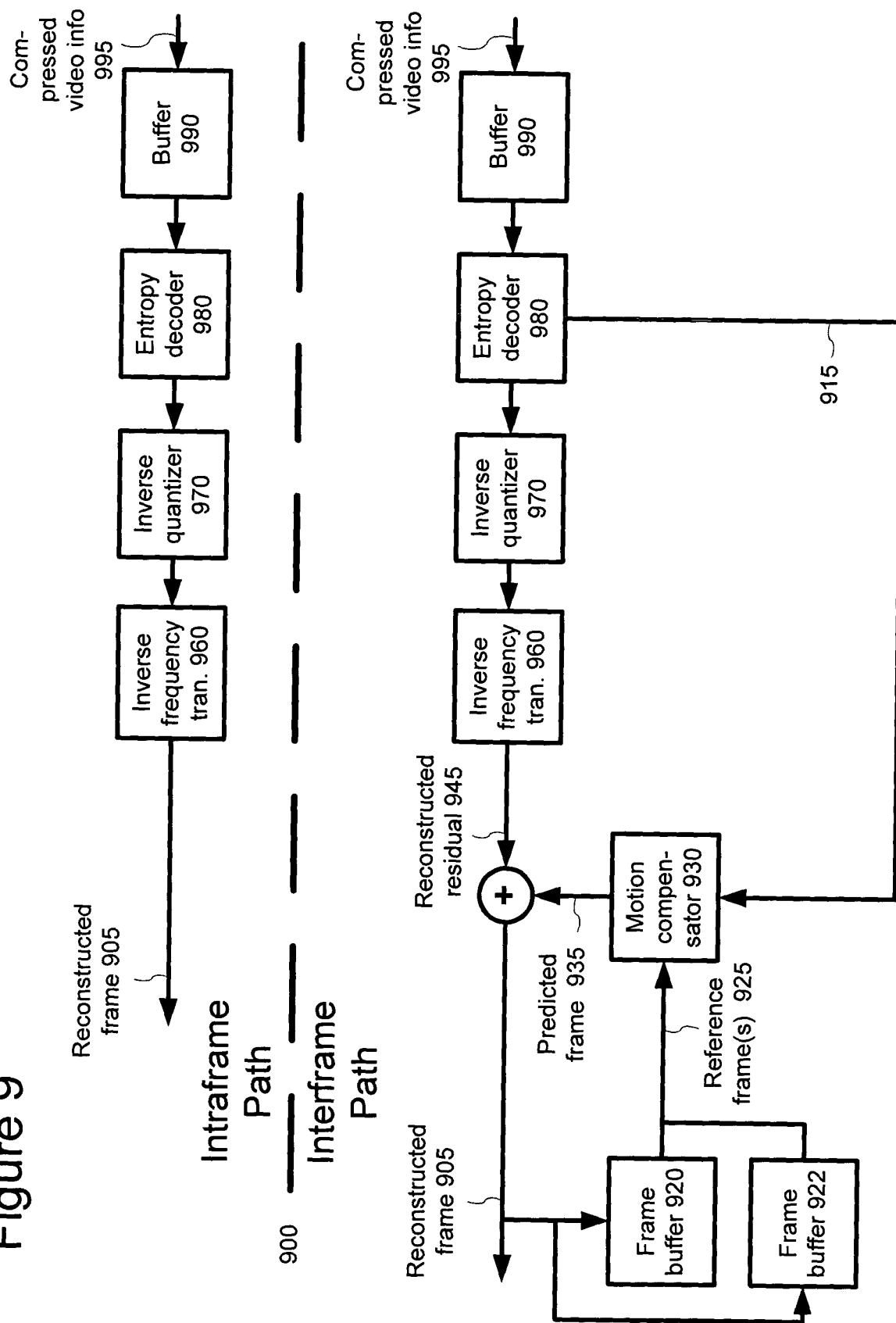


Figure 10

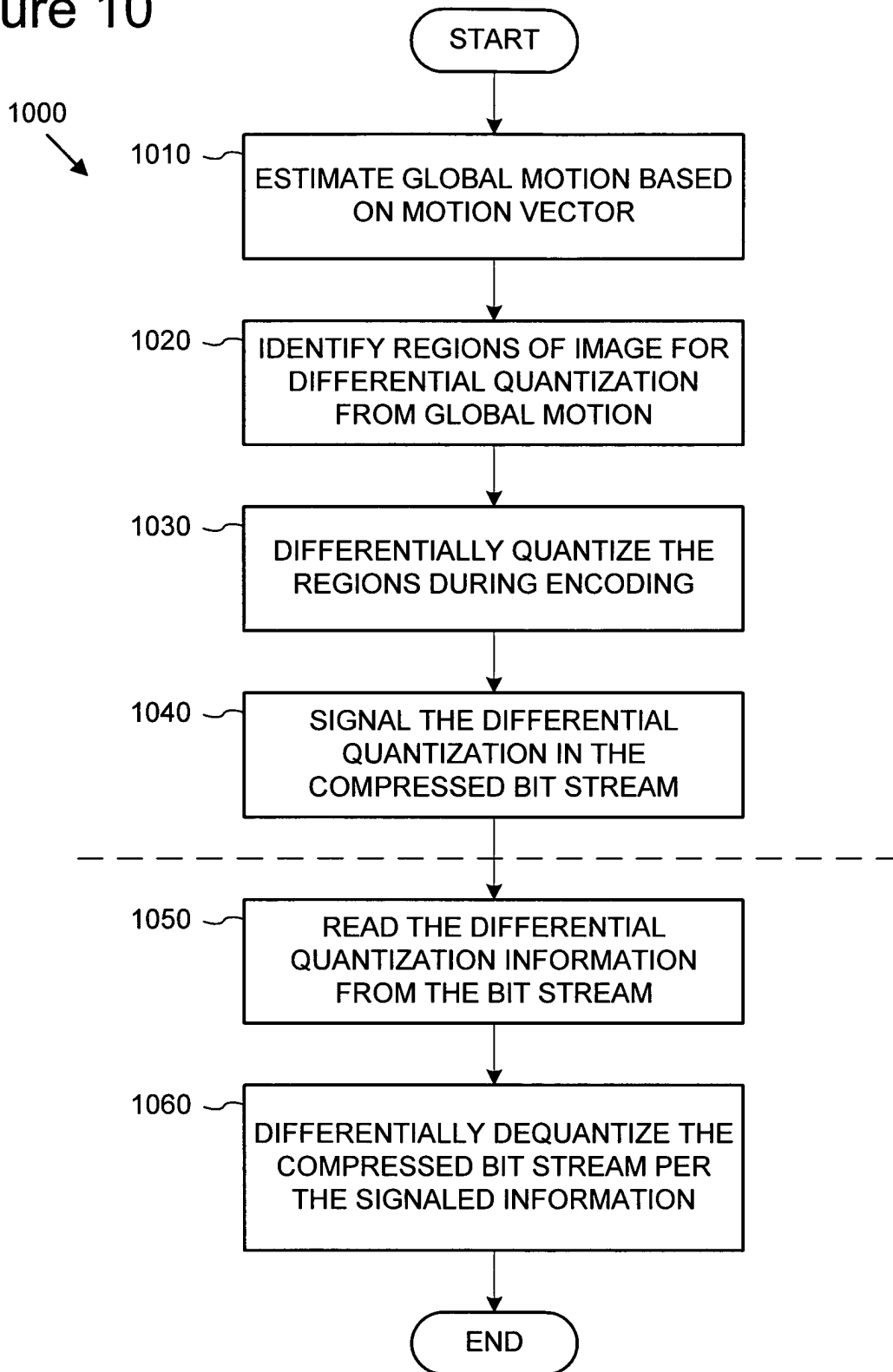


Figure 11

